

FIG. 1

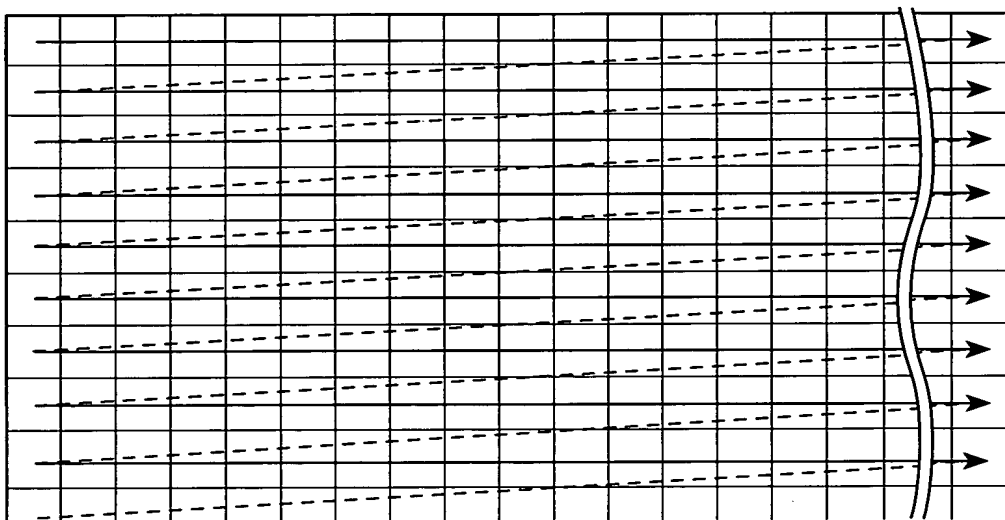
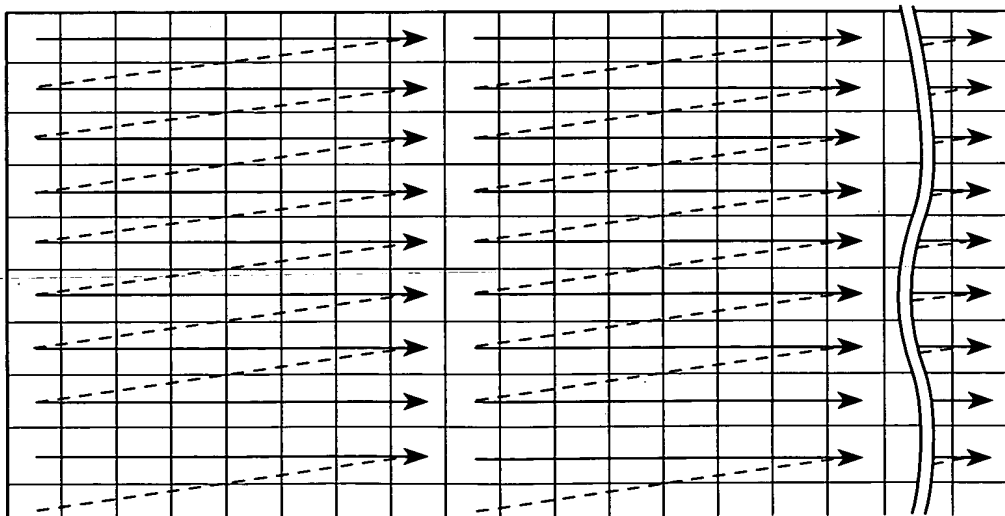


FIG. 2



[illegible]

FIG. 4
PRIOR ART

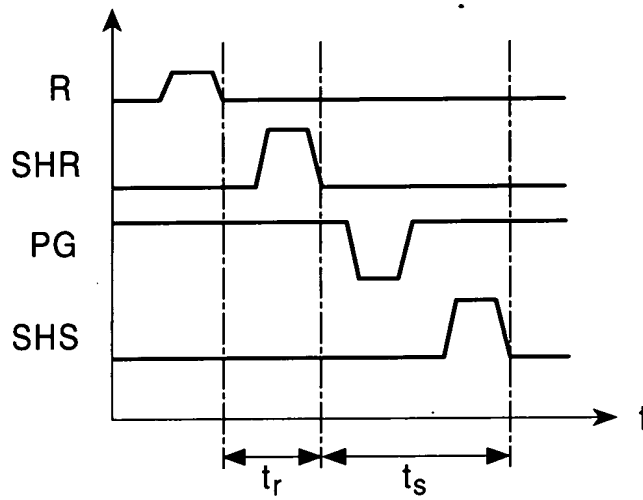


FIG. 5A
PRIOR ART

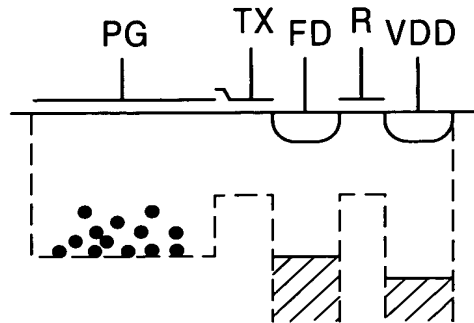


FIG. 5B
PRIOR ART

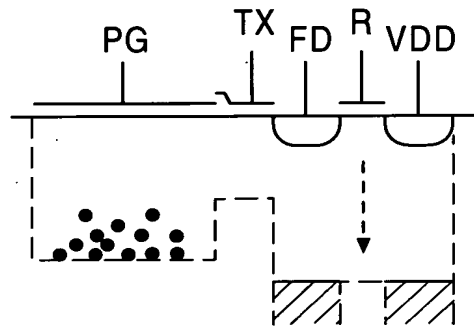


FIG. 5C
PRIOR ART

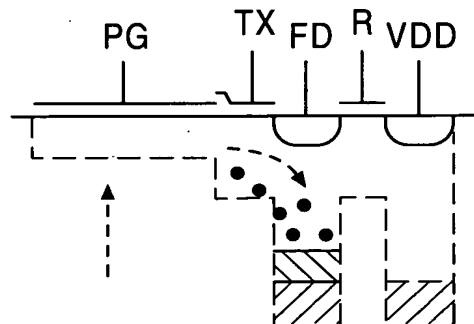
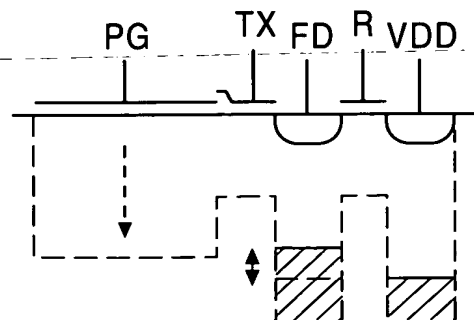


FIG. 5D
PRIOR ART



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FIG. 7

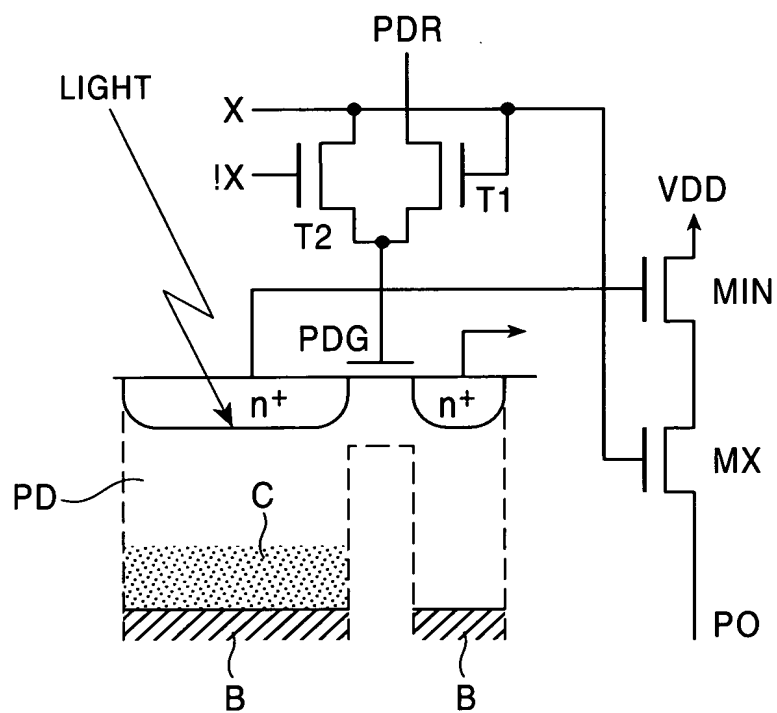


FIG. 8

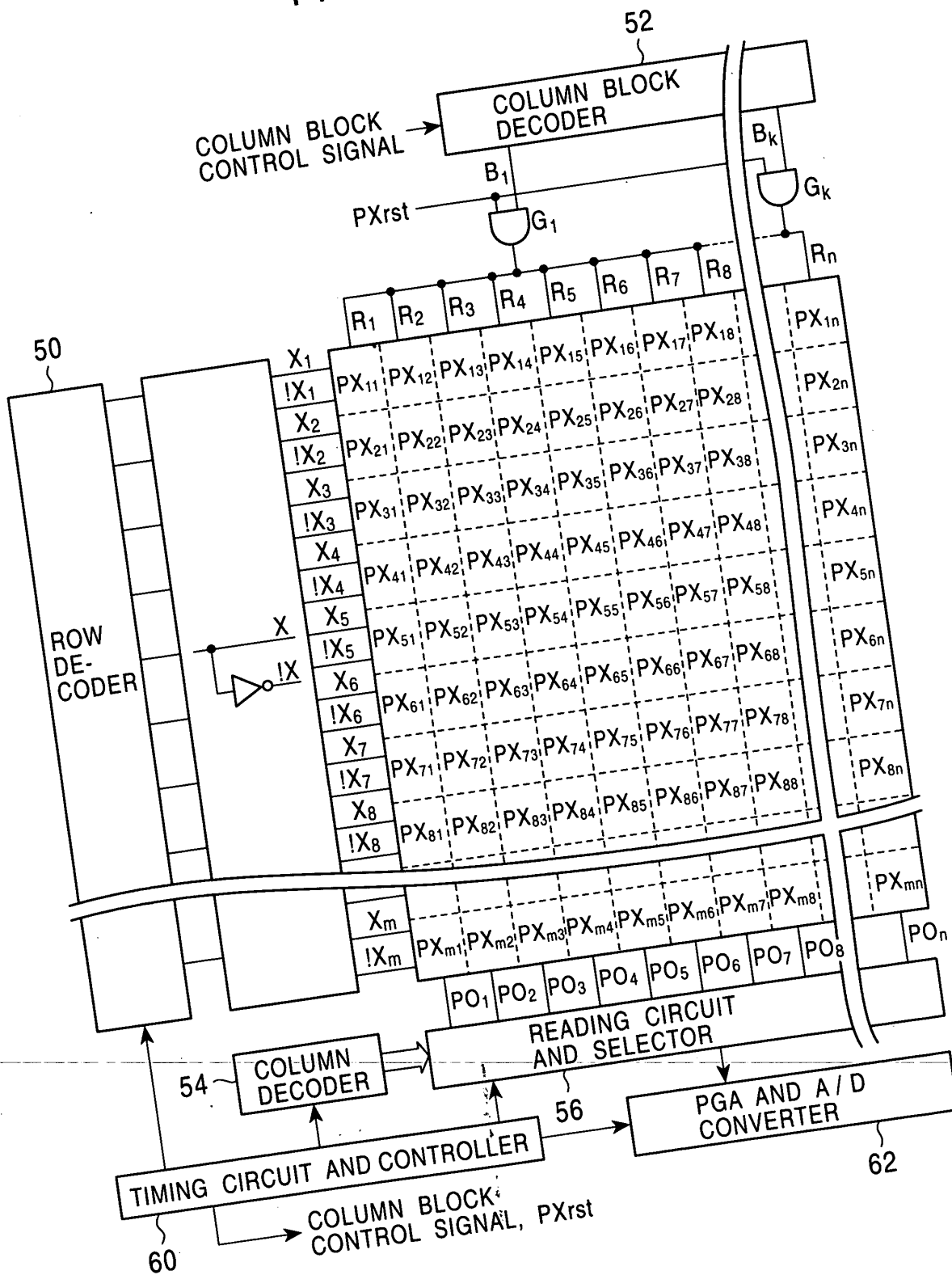


FIG. 9

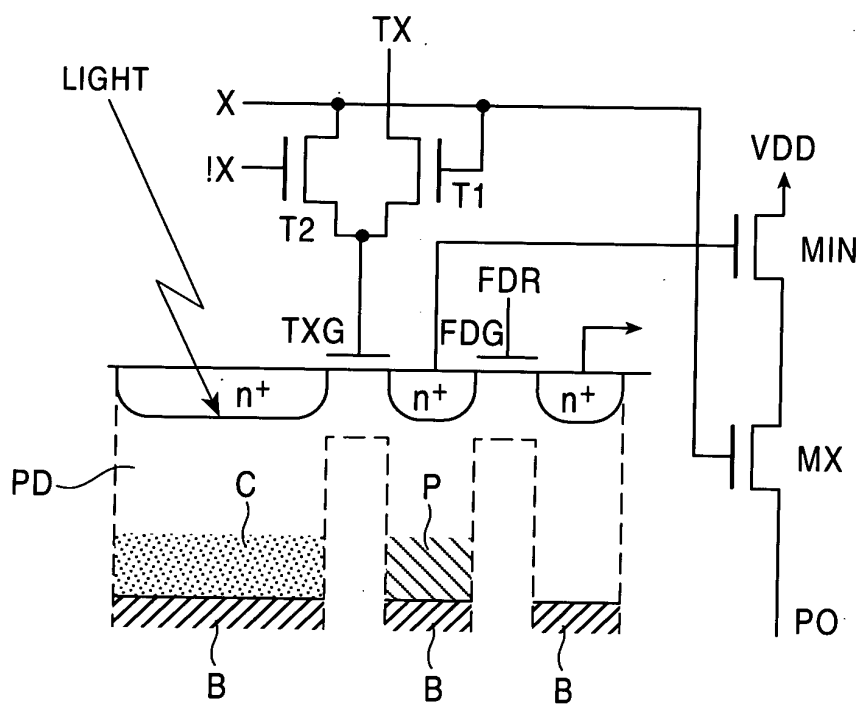


FIG. 10

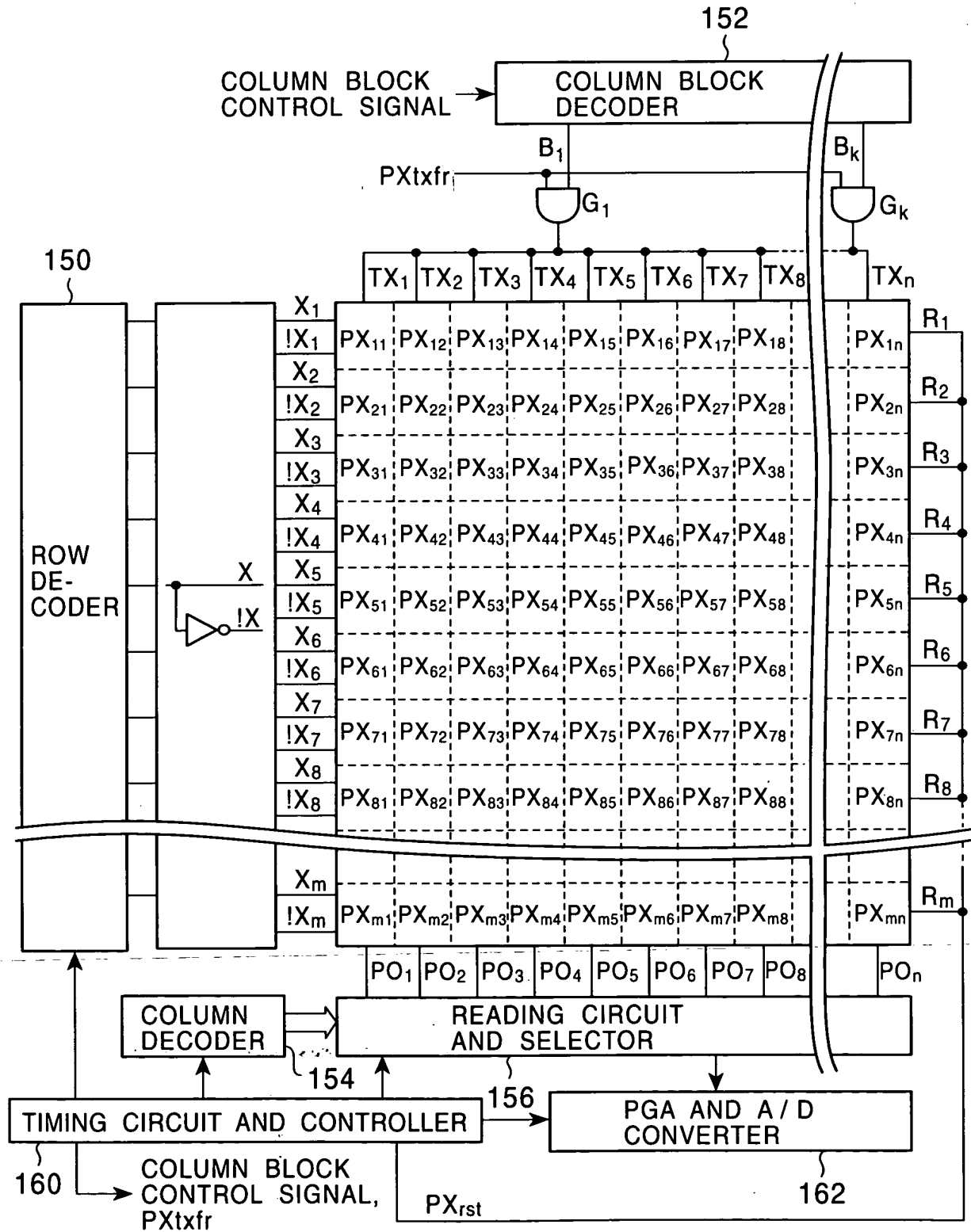


FIG. 11

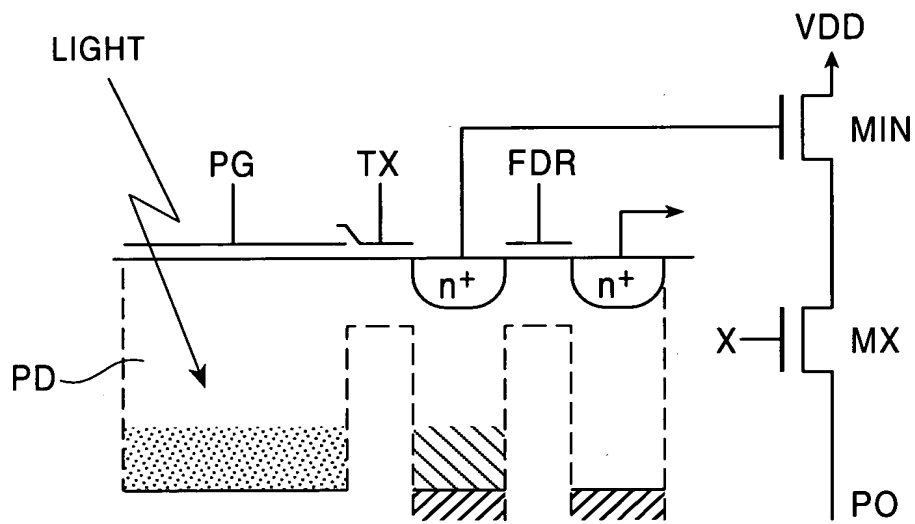


FIG. 12

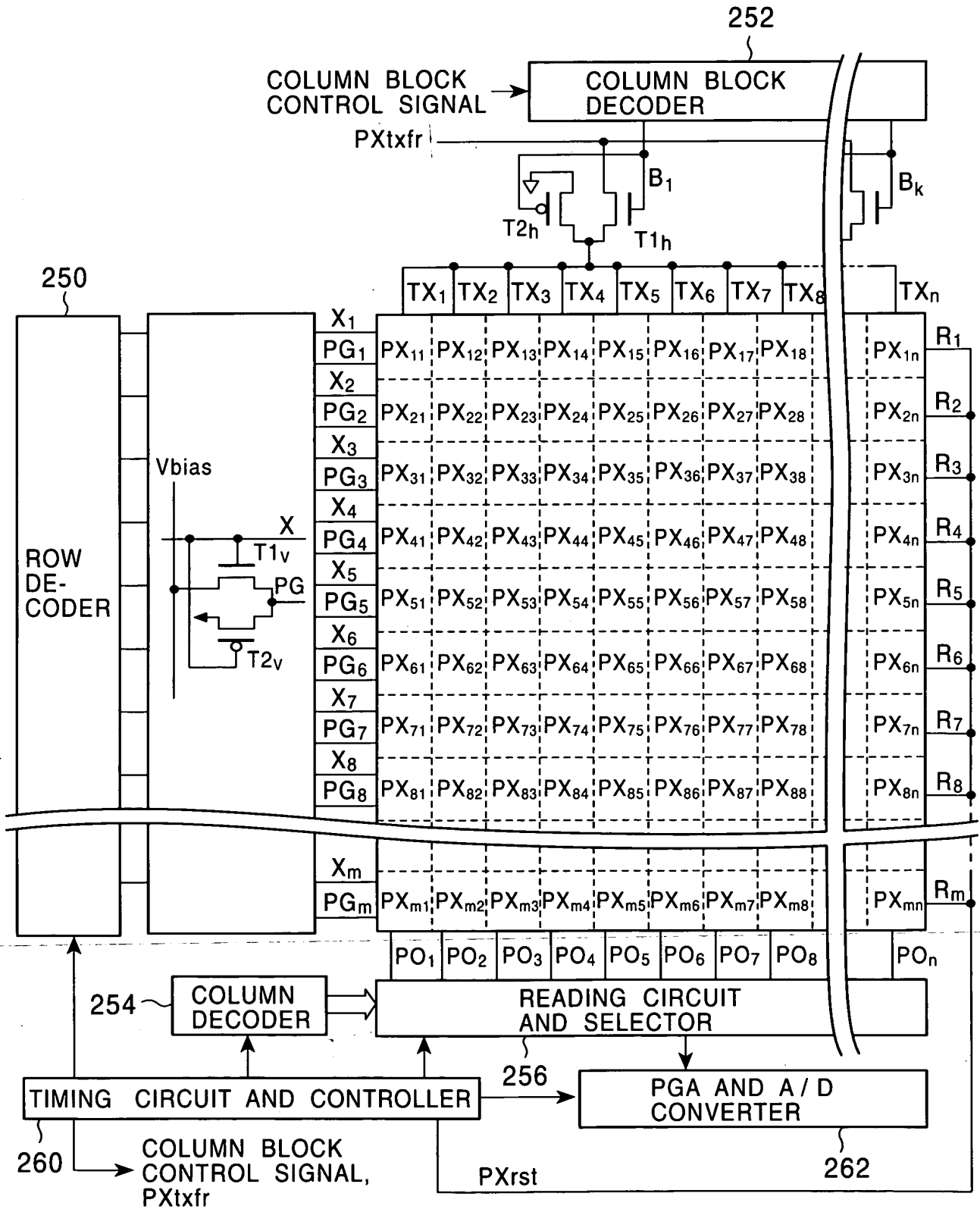
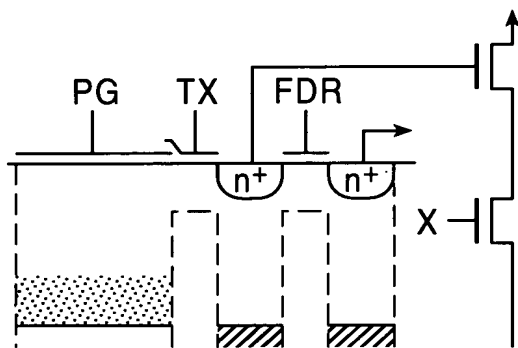
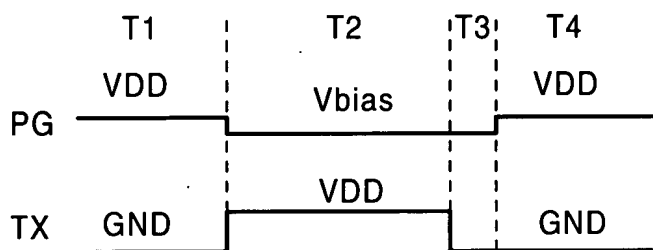
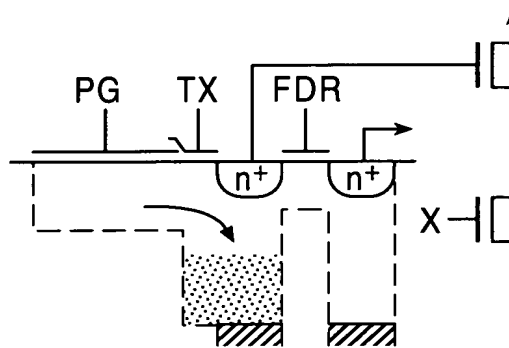


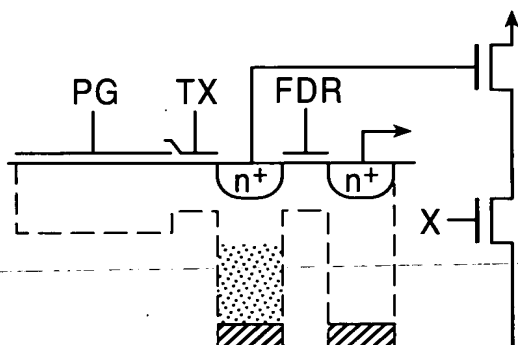
FIG. 13



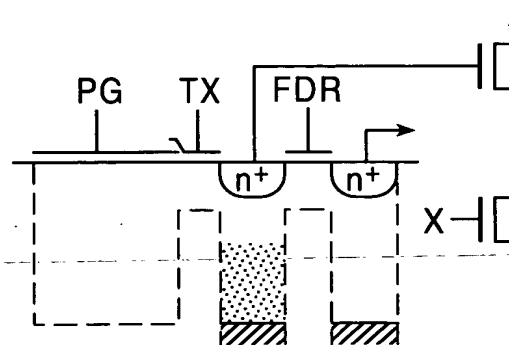
TIMING OF T1



TIMING OF T2



TIMING OF T3



TIMING OF T4

FIG. 14

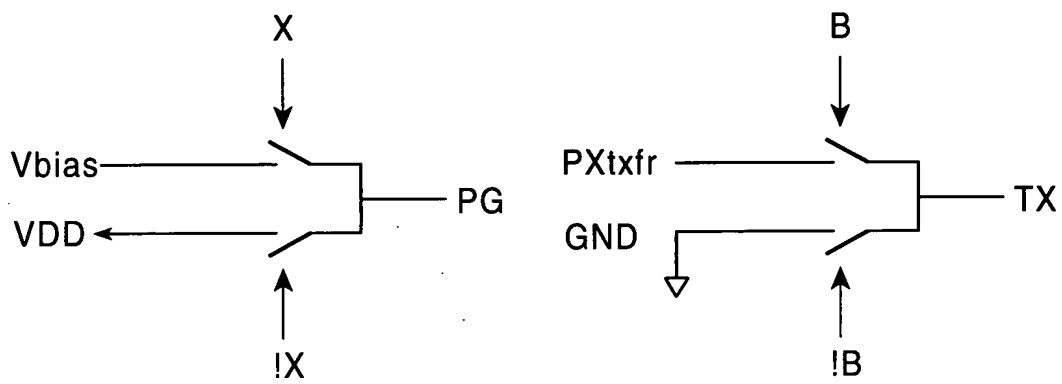


FIG. 15

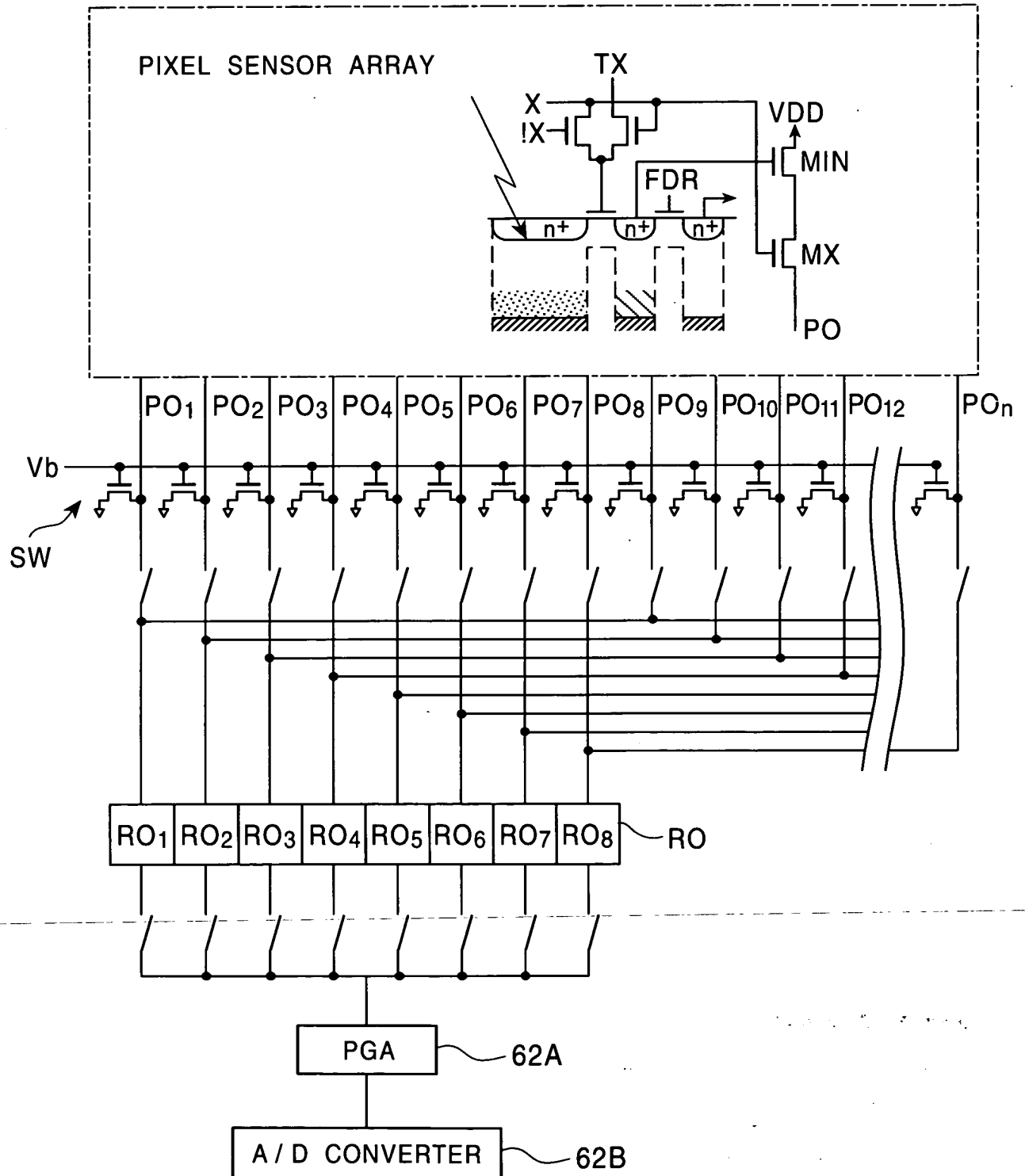
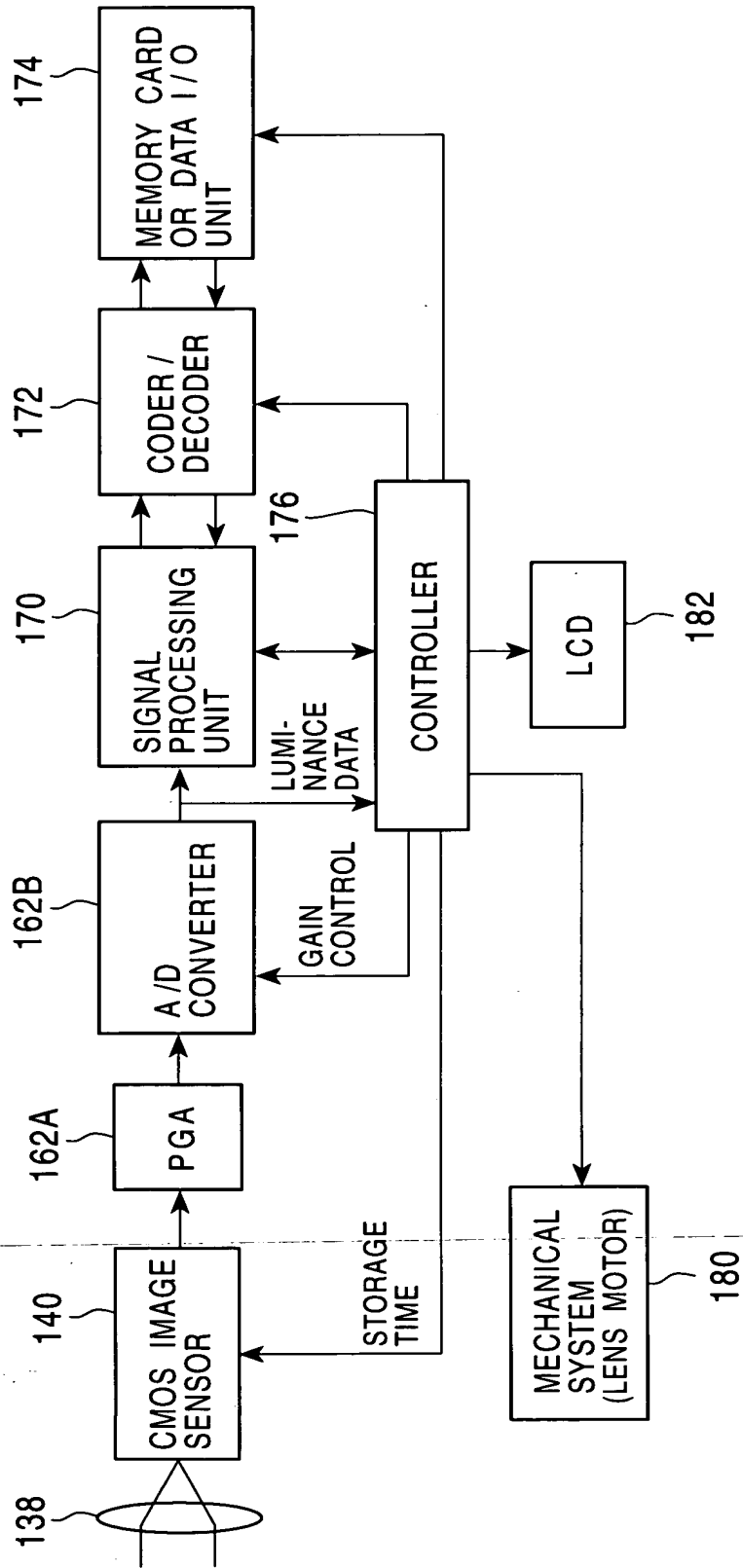


FIG. 16



WHITE LEVEL
(HIGHEST LEVEL)

BLACK LEVEL

FIG. 19

